

RADIO FREQUENCY IDENTIFICATION TAG ON FLEXIBLE SUBSTRATE

This application claims the benefit of U.S. Provisional Application No. 60/058,518, filed September 11, 1997.

This invention relates to identification systems
5 and tags therefor for producing a radio frequency
identification signal capable of being interpreted by an
electronic reader device.

BACKGROUND OF THE INVENTION

Commonly known identification systems use a reader
10 device which emits an interrogation signal such that a
proximate identification tag returns an identification
signal to the reader. Known types of identification tags
include passive non-electronic, e.g., bar coded, tags which
are visually identified by the reader according to an
15 imprinted pattern. Such tags and systems for manufacturing
such tags via an essentially continuous process have been
disclosed in U.S. Patent No. 5,615,504 to Peterson et al.
and U.S. Patent No. 5,609,716 to Mosher, Jr., both of which
are assigned to the assignee of the present invention. Such
20 systems require that the bar code be visible, i.e., within
the line-of-sight of the reader.

RFID (radio frequency identification) tags are
also well known which respond to a radio frequency
transmission from a reader to cause the tag to return an
25 electronic signal to the reader. For example, U.S. Patent
No. 5,493,805 to Penuela shows a flexible wrist band

2

mounting a memory chip or tag which can be accessed via a radio frequency signal and U.S. Patent No. 4,333,072 to Beigel shows exemplary circuitry for an RFID tag.

SUMMARY OF THE INVENTION

5 The present invention is directed to an enhanced identification tag for producing an identification (ID) signal, i.e., a radio frequency (RF) signal carrying identification information, capable of being interpreted by an electronic reader device, spaced from, but in the
10 vicinity of, e.g., one to twelve feet, the tag. An identification tag in accordance with the invention is characterized by a flexible substrate, programmable encoder circuitry formed on said substrate defining identification information, an antenna, and signal generator circuitry
15 carried by said substrate responsive to said encoder circuitry for applying a radio frequency signal bearing said identification information to said antenna.

 In accordance with one aspect of the invention, the programmable encoder circuitry is comprised of
20 conductive paths selectively formed on the substrate, e.g., by conductive ink printing, metal deposition, or other techniques suitable for continuous line manufacturing.

 In accordance with a different aspect of the invention, the signal generator circuitry includes one or
25 more electronic switches and/or amplifier devices, e.g., field effect transistors (FETs), formed on said substrate by

printing or analogous techniques suitable for continuous line manufacturing.

In accordance with a further aspect of the invention, the signal generator circuitry includes one or
5 more reactance (inductance and/or capacitance) elements formed on said substrate by printing or analogous techniques suitable for continuous line manufacturing.

An identification tag in accordance with the invention can be triggered to transmit its ID signal in
10 various manners and is thus suitable for use in multiple types of identification systems. For example, the system can use an interrogator-reader device which generates an interrogation signal to cause the tag to return an ID
15 signal. Alternatively, the tag can be configured to transmit its ID signal in response to some other event such as the end of a timed interval.

The novel features of the invention are set forth with particularity in the appended claims. The invention will be best understood from the following description when
20 read in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

FIGS. 1 and 2 show an exemplary prior art
identification device comprising a wrist
band having bar-coded identification
25 information imprinted thereon;

4

FIG. 3 is a schematic representation of a prior art continuous line manufacturing process for fabricating the identification device of FIGS. 1 and 2;

FIG. 4 shows a simplified block diagram of an identification tag in accordance with the present invention for producing a radio frequency signal bearing identification information;

FIG. 5 is a circuit schematic diagram of a tag in accordance with the invention comprised of selectively enabled inductor-capacitor (LC) components, suitable for fabrication via printing techniques;

FIGS. 6A and 6B are respectively top and bottom views of printed portions of the passive tag of FIG. 5;

FIG. 7 is a circuit schematic diagram of tag circuitry, e.g., a sequential counter, comprised of semiconductor switches and/or amplifiers that are selectively enabled to emit a radio frequency signal bearing identification information;

FIGS. 8A-8C respectively show the top and bottom sides of a middle flexible substrate layer and a top side of an upper laminate,

collectively defining the circuitry of
FIG. 7;

FIG. 9 shows a blow up view of three laminated
layers of flexible substrate material

5 which form the tag of FIG. 7; and

FIG. 10 shows a schematic of an apparatus (a
modification of that shown in FIG. 3) for
continuous line manufacturing the
preferred tags of FIGS. 5-9.

10 DESCRIPTION OF THE PREFERRED EMBODIMENTS

The present invention is directed to an enhanced
identification tag for producing an identification (ID)
signal, i.e., a radio frequency (RF) signal carrying
identification information, capable of being interpreted by
15 an electronic reader device. An identification tag in
accordance with the invention is characterized by a flexible
substrate, programmable encoder circuitry formed on said
substrate defining identification information, an antenna,
and signal generator circuitry carried by said substrate
20 responsive to said encoder circuitry for applying a radio
frequency signal bearing said identification information to
said antenna.

A tag in accordance with the invention is
preferably fabricated using continuous line manufacturing
25 techniques, e.g., printing, to form a conductive pattern,
e.g., comprised of a conductive ink based on silver, carbon,

etc., on a flexible substrate, e.g., polytethyline,
polyvinyl chloride or other plastic type material. It
should be understood that the term "printing" is intended to
broadly connote any fabrication technique for forming,
5 depositing or otherwise laying down a path of conductive
material.

FIGS. 1 and 2 correspond to FIGS. 1 and 2 of U.S.
Patent No. 5,609,716 and depict an exemplary prior art
identification device 10 comprising a flexible wristband 12
10 bearing identification information, typically visually
readable bar code imprinted information 14. The device of
FIGS. 1 and 2 is suitable for manufacture in a continuous
line manufacturing process as depicted in FIG. 3. The use
of bar code technology or other visually readable techniques
15 is limited to those applications where it is practical to
maintain line-of-sight between the identification device and
a reader. In applications where line-of-sight cannot be
maintained, it is useful to employ radio frequency
technology, e.g., as discussed in U.S. Patent No. 4,333,072.

20 The present invention is directed to radio
frequency identification (RFID) tags suitable for use with
wristbands and the like which can be manufactured using a
continuous line process as is generally depicted in FIG. 3.

In a typical application, the RFID tag can be used
25 to identify a person to permit, deny or otherwise determine
the level of access to an area, e.g., a concert, a work area

or other restricted environment or to convey descriptive information about a person, e.g., for hospital patient management.

FIG. 4 shows an exemplary tag 20 in accordance with the invention primarily comprised of ~~a~~ (1) a flexible substrate 22, (2) antenna 24, (3) signal generator circuitry 26 carried by the flexible substrate 22 and ~~(4)~~ ⁺ programmable encoder circuitry 28 formed on the flexible substrate 22.

In a typical mode of operation, a reader 30 emits a radio frequency (RF) interrogation signal (typically between 100 KHz and 3 GHz) via path 32 which is received by the antenna 24. The received interrogation signal is coupled via path 34 to the signal generator circuitry 26 which generates a radio frequency identification signal bearing identification information defined by the encoder circuitry 28. The identification signal is applied to the antenna 24 via path 36 which transmits it back to the reader 30 via path 38.

The encoder circuitry 28 can be implemented in various manners in accordance with the invention to satisfy particular application requirements. For example, the circuitry 28 can function as a read-only memory programmed at the time of manufacture or alternatively as a read-write memory which is nonvolatile but alterable. A read-write capability is useful for writing in information into the tag subsequent to manufacture, for example, at the time the tag is issued to an individual. Various well known techniques

can be used to write, i.e., alter, the memory to define identification information. Specific embodiments of the encoder circuitry 28 can, of course, also include both read-only memory portions as well as read-write memory portions.

5 In a typical read-only implementation, the encoder circuitry 28 can comprise a plurality of selectively formed electrical connections 40 to the signal generator circuitry 26 by forming electrical connections to the signal generator circuitry inputs, e.g., by printing conductive ink on the
10 flexible substrate 22. Consequently, the operation, e.g., the timing, of the signal generator circuitry 26 is modified by the identification information defined by the encoder circuitry 28 to generate the identification signal. In an alternative implementation, the encoder circuitry 28 can
15 include a semiconductor memory, e.g., a read-only device or a read-write device (either volatile or nonvolatile) such that data stored within the memory defines the identification information. Such a semiconductor memory can be configured to enable it to be remotely programmed, e.g.,
20 by a radio frequency command signal.

The signal generator circuitry 26 can be implemented as either active (power consuming, e.g., semiconductor switches and/or amplifiers) or passive (i.e., reflective) circuitry. For active implementations, a power
25 supply 42, e.g., a battery, is used to supply power to the signal generator 26. Alternatively, the power supply 42 can

a extract power from the received RF signal ~~via path 34~~ (see U.S. Patent No. 4,333,072 to Beigel).

In an alternative implementation of the signal generator, the signal generator 26 can emit a signal in response to a specific trigger event, e.g., an input from a timer 44 or sensor input in the tag. In such an implementation, the signal generator 26 generates the radio frequency signal bearing the identification information to the reader 30 via path 38 without requiring the interrogation signal on path 32.

The signal generator circuitry 26 is carried by, i.e., mounted proximate to or formed on, the substrate 22. In a preferred embodiment shown in FIG. 5, the signal generator circuitry 26 is implemented via printing techniques using conductive ink on the flexible substrate 22. In this embodiment, a delay line 50, coupled to antenna 24 (essentially an inductor), is selectively formed of a plurality of inductors (L) 52 and capacitors (C) 54.

Conductive ink 55 is imprinted on the flexible substrate 22 to selectively enable portions of the circuit, i.e., capacitors 54 which form each LC portion of the delay line 50, which define the identification signal. While this embodiment shows signal generator circuitry 26 implemented from reactance elements, i.e., inductors and capacitors, which reflect a modified interrogation signal as the identification signal, it will be additionally described

below how to implement active signal generator circuitry using similar printing techniques that instead emits an identification signal or reflects a modified interrogation signal based on varying the power absorption by the tag of
5 the interrogation signal (see U.S. Patent No. 4,333,072).

For example, using an exemplary half-duplex transmission protocol, only one portion of a tag/reader pair transmits a signal at a given time. For instance, the reader transmits an interrogation signal which is rectified
10 and stored in a capacitor inside the tag. The reader then shuts off its signal and sets itself to receive incoming (low level) signals and the tag transmits its identification signal using the stored energy in the capacitor. The process repeats until successful reception of the
15 identification signal by the reader. Alternatively, using an exemplary full-duplex transmission protocol, the reader and the tag are active simultaneously. The reader continuously transmits an energizing signal to the tag which is rectified and stored in a capacitor, typically smaller
20 than that used in an exemplary half duplex tag. The tag transmits an identification signal by sequentially variably loading its antenna coil in a pattern corresponding to the identification code. The reader receives and interprets the identification signal during the transmission of the
25 interrogation signal.

In operation, antenna 24 receives the electromagnetic signal via path 32 and passes this received signal via path 34 to the delay line 50. In response to the received signal on path 34, the delay line 50 reflects back
5 a unique (dependent upon the enabled LC legs) identification signal to the antenna 24, detectable by the remote reader 30.

FIGS. 6A and 6B respectively show exemplary patterns suitable for etching on top 56 and bottom 58 sides
10 of the flexible substrate 22 to form the circuitry of FIG. 5. The RFID circuitry, i.e., the encoder circuitry 28 and signal generator circuitry 26, is implemented by printing patterns on the flexible substrate 22 using conductive ink 55. For example, inductors 52 are formed by a preferably
15 curved path printed on one side of the substrate 22 while capacitors 54 are implemented by printing a first conductive plate 60 on the top substrate side 56 and a second conductive plate 62 on the bottom substrate side 58, thus forming two conductive plates 60, 62 separated by a
20 dielectric, i.e., the substrate 22. By selectively printing/depositing second conductive plates 62 on the bottom substrate side 58, capacitors 54 can be selectively formed to change the reflection characteristics of the delay line 50, i.e., to form the encoder circuitry 28. For
25 example in FIG. 6B, while the second conductive plate 62a is present and thus capacitor 54a exists, the second conductive

plate 62b is absent and thus capacitor 54b is also absent.

The antenna 24 is shown in FIG. ⁵ as a single inductor.

However, the exemplary antenna 24 of FIG. 6 actually shows a pair of inductors 64 and 66 coupled by a centrally located

5 capacitor 68. Alternatively, a feedthrough (not shown) can be drilled or punched through the substrate 22 and filled with conductive ink 55 to form a single inductor antenna 24 as shown in FIG. 5.

Optionally, visually identifiable data can also be
10 printed on the substrate 22, e.g., a picture 70 and/or a bar-coded pattern 72. Alternatively, a conductive bar-coded pattern can be used to provide electrical connections to determine the identification signal, i.e., perform the function of the conductive ink 55, as well as providing a
15 means to visually identify the tag. To protect and isolate the conductive patterns from electrical interaction with a tagged object, e.g., a person's wrist, additional layers of flexible material are preferably laminated to the top 56 and bottom 58 substrate sides (see FIG. 10).

20 In another preferred embodiment, a pattern of conductive, e.g., graphite based, semiconductive and insulating polymers can be printed or otherwise deposited on the flexible substrate 22 to form the signal generator 26 and/or the encoder circuitry 28 from a plurality of
25 semiconductor switches and/or amplifiers, e.g., field effect transistors (FETs). An exemplary technique for forming such

a device, referred to as an organic semiconductor, is described in an article by Garnier et al. entitled "All-Polymer Field-Effect Transistor Realized by Printing Techniques" (Science, Vol. 265, 16 September 1994) which is
5 incorporated herein by reference. Forming an RFID tag from active circuitry, e.g., FETs or other transistors, present significant advantages. For example, while the reflective circuitry shown in FIG. 5 is an effective circuit for unique detection by a reader 30, some limitations do exist.

10 Typically, reflective circuitry will have a limited range and the characteristics of the reflected signal will be limited to the frequency range and time period of the electromagnetic interrogation signal on path 32. However, active circuitry can respond to the interrogation signal by
15 emitting an identification signal having different frequency characteristics and/or the signal can be delayed a predetermined period of time from the received interrogation signal. Additionally, active circuitry can present a higher energy identification signal with a higher data content that
20 is more identifiable by the reader 30.

Additional technologies are available for creating additional components as part of the RFID tag circuitry by using printing techniques on a flexible substrate. For example, a battery is described in an article by Davis
25 entitled "Johns Hopkins Scientists Create All-Polymer Battery" (PCIM February 1997), LEDs, i.e., diodes,

14

fabricated from organic semiconductors are also known in the art, and resistors can be implemented by using different compositions for lines of conductive ink printed on the substrate, restricting the printed line width or extending
5 its length.

FIG. 7 shows an exemplary implementation of an RFID tag using active circuitry (similar to that shown in U.S. Patent No. 4,333,072) suitable for implementation by printing fabrication techniques on a flexible substrate.

10 Essentially, antenna 24 receives an electromagnetic interrogation signal which is half-wave rectified by the combination of diode 74 and capacitor 76 to provide power (V+) to active signal generator circuitry 78, implemented as organic semiconductors (e.g., a plurality of semiconductor
15 switches or amplifiers 80 used to form circuitry 78, e.g., a sequential counter as described in U.S. Patent No.

4,333,072). Alternatively, a battery ~~82~~ can power the signal generator circuitry 78. The interrogation signal 32 is coupled to the signal generator circuitry 78 via path 84
20 where it is used as a clock input. Dependent upon which inputs 86 are connected to ground 88 using pads of conductive ink 55 (thus comprising encoder circuitry), the signal generator circuitry 78 outputs an identification signal via path 89 that activates load circuit 90

25 (preferably comprised of a FET and an optional load

resistor) in a predetermined sequence. This loading of the received signal can be remotely detected by the reader 30.

FIGS. 8 and 9 show an exemplary implementation of the circuitry of FIG. 7 as a laminate of three flexible

5 substrate layers 92, 94 and 96, having conductive, semiconductive and insulating polymers imprinted thereon to form organic semiconductors, capacitors and inductors. A middle flexible substrate layer 94 has polymer patterns printed on its top 98 and bottom 100 surfaces. The
10 inductors and capacitors are formed as previously described in reference to FIG. 6 while the load circuit 90 and the signal generator circuitry 78 are formed as organic semiconductors as described by Garnier et al. Pads between inputs 86 to the signal generator circuitry 78 and ground 88
15 are left open on the top surface of the middle substrate layer 94. To protect the deposited circuitry, a top substrate layer 92 is laminated above the middle substrate layer 94 and a bottom substrate layer 96 is laminated below (see also FIG. 10). However, the top substrate layer 92
20 additionally has a window 102 corresponding to the position of the input 86 and ground 88 pads. Consequently, conductive ink 55 can be printed or otherwise deposited through the window 102 to programmably encode the signal generator circuitry 78, i.e., defining the encoder circuitry
25 28. Typically inputs 86 are high impedance inputs, e.g., when the signal generator circuitry 78 is FET based. Thus,

the choices for the conductive ink 55 are greatly expanded, i.e., a large resistivity range of inks are acceptable.

Such a tag structure is suitable for continuous line manufacturing. Alternatively, such a tag can be formed in discrete steps. For example, the three layer laminate comprised of layers 92, 94 and 96 can be formed first including the depositions associated with the middle substrate layer 94 and at a later time and/or location, e.g., where the tags are distributed, the tag can be programmably encoded through the window 102.

FIG. 10 shows a schematic of an exemplary apparatus 104 (a modification of that shown in FIG. 3) for manufacturing the preferred tags 20 of FIGS. 4-8. In operation, the flexible substrate 22 is dispensed from roll 106 and fed past a plurality of process stations 108a-n which imprint the conductive, semiconductive and insulating patterns required to add connections and/or define circuitry which comprise the RFID tag circuitry as described above. The process stations 108 perform various functions, depending on the process, e.g., to imprint various conductive, semiconductive and insulating ink layers, to dry the ink, etc. After imprinting, additional layers of flexible materials 110, 112 are preferably laminated using laminating station 114 to the substrate 22. Fastening means 116 are then preferably attached to or formed on the substrate 22 and the substrate 22 is cut using cutting means

118 to form the completed identification tag 20. Such apparatus 104, can operate either upon demand, i.e., when a new tag is required, or at an essentially continuous rate for continuous line manufacturing of a plurality of uniquely
5 identifiable tags 20. Alternatively, the tag can be manufactured at the factory but not fully programmed. The partially programmed tags can be shipped in continuous rolls to deployment locations where a separate programming fixture (not shown) can complete the tag programming upon demand,
10 e.g., by depositing conductive ink pattern as previously described and/or other visual identifying information.

Although the present invention has been described in detail with reference only to the presently-preferred embodiments, those of ordinary skill in the art will
15 appreciate that various modifications can be made without departing from the invention.

18